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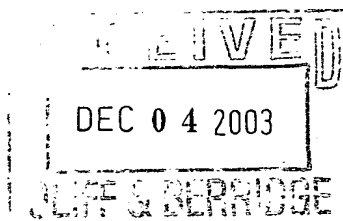


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/625,746	07/24/2003		750	115816	5	14	3

25944  
OLIFF & BERRIDGE, PLC  
P.O. BOX 19928  
ALEXANDRIA, VA 22320



CONFIRMATION NO. 8139

FILING RECEIPT



\*OC000000011400884\*

Date Mailed: 12/03/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hatsuki Kanbayashi, Sakata-shi, JAPAN;  
*Higashitagawagun*

Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

D mestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-216661 07/25/2002

If Required, Foreign Filing License Granted: 12/01/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor devices, and manufacturing methods, circuit substrates and electronic equipments for the same



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hatsuki KANBAYASHI

Application No.: 10/625,746

Docket No.: 115816

Filed: July 24, 2003

For: SEMICONDUCTOR DEVICES, AND MANUFACTURING METHODS, CIRCUIT  
SUBSTRATES AND ELECTRONIC EQUIPMENTS FOR THE SAME

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Thomas J. Pardini  
Registration No. 30,411

JAO:TJP/mlo

Date: January 14, 2004

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**

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